

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant** : Yoshitsugu MORITA et al.  
**Serial No.** : 10/599,041  
**International Serial No.** : PCT/JP2005/04410  
**International Filing Date** : 03/08/2005  
**Priority Date** : 03/19/2004  
**Confirmation No:** 3380  
**Attny No.** : 71,051-071  
**Title** : Semiconductor Device And Method Of Manufacturing Thereof

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**RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS**  
**UNDER 35 U.S.C. 371 IN THE UNITED STATES**  
**DESIGNATED/ELECTED OFFICE (DO/EO/US)**

**Mail Stop BOX PCT**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US) dated May 10, 2007, and in compliance with 37 CFR 1.497(a) and (b), identifying the application by the International application number and International filing date, Applicant is submitting herewith the required Oath or Declaration of the inventors.

Also enclosed herewith is a copy of the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US) as requested.

In addition, Applicant is also submitting the required petition fee under 37 CFR 1.492(h) of \$ 130.00. The Commissioner is also hereby authorized to charge any additional fees or credit our deposit account 08-2789 for any overpayments.

**Respectfully submitted,  
HOWARD & HOWARD ATTORNEYS, P.C.**

July 10, 2007

Date

**/David M. LaPrairie/**

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